

REMARKS

Claims 1-9, 11-30, 32-36 and 44-46 are pending with claims 1-4, 12 and 13 being independent. Claims 10, 31 and 37-43 have been previously cancelled. Claims 2, 3, 12 and 13 have been amended to recite a feature that is also recited in claim 1. No new matter has been introduced.

Claims 1-9, 11-30, 32-36 and 44-46 have been rejected on the ground of nonstatutory obviousness-type double patenting as being unpatentable over claims 1-25 of U.S. Patent Number 6,798,148.

Independent claim 1 recites, among other features, that “the plurality of transistors are connected in series when inputting current as a first step and the plurality of transistors are connected in parallel when outputting current as a second step.” Applicant requests reconsideration and withdrawal of the rejection of claim 1 and its independent claims because the claims of the ‘148 patent do not describe or suggest connecting the transistors in series when inputting current and connecting the transistors in parallel when outputting current.

Similarly to claim 1, each of independent claims 2, 3, 12 and 13 has been amended to recite that the transistors are connected in series when inputting current and in parallel when outputting current. Accordingly, applicant requests reconsideration and withdrawal of these claims and their dependent claims for the reasons discussed above with respect to claim 1.

Independent claim 4 recites, among other features, “when a current is inputted to the electronic circuit, as for a k^{th} transistor ($k=2$ to less than n) in the n transistors, a current flows through a $(k - 1)^{\text{th}}$ transistor to a $(k + 1)^{\text{th}}$ transistor via the k^{th} transistor [and] when the current is outputted in the electronic circuit, as for the k^{th} transistors, the current flows from the side connected to the second switch to the side connected to the first switch.” Applicant requests reconsideration and withdrawal of the rejection of claim 4 and its dependent claims because the claims of the ‘148 patent do not describe or suggest this feature.

Applicant submits that all claims are in condition for allowance.

Applicant : Kazutaka Inukai
Serial No. : 10/743,344
Filed : December 23, 2003
Page : 10 of 10

Attorney's Docket No.: 12732-201001 / US6877

No fee is believed to be due at this time. Please apply any other charges or credits to deposit account 06-1050.

Respectfully submitted,

Date: 4/9/07


John F. Hayden
Reg. No. 37,640

Fish & Richardson P.C.
1425 K Street, N.W.
11th Floor
Washington, DC 20005-3500
Telephone: (202) 783-5070
Facsimile: (202) 783-2331

40410774.doc